

**Amendments to the Specification:**

Please replace the paragraph beginning on page 5, line 7, with the following amended paragraph:

Figure 1 shows a block diagram of an engine-control device, which is meant to constitute an example of a microprocessor system according to the exemplary embodiment of the present invention. Connected to a bus 1 on a printed circuit board are a one-chip microcomputer 2; one or a plurality of storage modules, each of which is identified by an individual serial number, only one flash memory 3 being shown in the figure for the sake of simplification; a control module 4 which may be implemented as ASIC, for instance; a write-read storage module 5; and an interface 6 for the communication with sensors and actuators (not shown) of the engine to be controlled. Modules 2, 3, 4, 5 are each formed by IC's, which are separate from each other. Integrated in the chip of microcomputer 2 are a microprocessor 10 and a read-only program memory 11. An internal bus 12 via which microprocessor 10 and program memory 11 communicate is ~~preferably not brought out of~~ entirely contained within chip 2, so that an unauthorized person may not read off the contents of program memory 11 outside of the microcomputer chip. Program memory 11 includes, in particular, program commands for a boot procedure of the control device.